## PATENT ABSTRACTS OF JAPAN

(11)Publication numb r:

04-175299

(43)Dat of publication of application: 23.06.1992

(51)Int.CI.

C30B 29/68 C30B 25/02 C30B 29/40 // H01L 21/205

(21)Application number: 02-302632

(71)Applicant: FUJITSU LTD

(22)Date of filing:

09.11.1990

(72)Inventor: OTSUKA NOBUYUKI

**OZEKI MASASHI** 

KODAMA KUNIHIKO SAKUMA YOSHIKI

## (54) COMPOUND SEMICONDUCTOR CRYSTAL GROWTH AND COMPOUND SEMICONDUCTOR DEVICE (57)Abstract:

PURPOSE: To produce a compound semiconductor layer having hetero interfaces so as to acquire such steep hetero interfaces as to have a precision of monatomic layer, by feeding, using the atomic layer epitaxy technique, a semiconductor substrate with the feedstocks of elements constituting the title compound semiconductor in high flow rate at low-growth temperature and low-growth pressure.

CONSTITUTION: Using the atomic layer epitaxy technique, a semiconductor substrate is alternately fed with the feedstocks of elements constituting the objective compound semiconductor to effect monatomic layer growth of the superlattice structure layer of (GaAs)m(GaP)n, (GaAs)m(GaAsP)n, (GaAsP)m(GaP)n or (GaAs)l (GaAsP)m(GaP)n (I, m and n are each positive integer). It is preferable that the feedstocks for Ga, As and P be trimethylgarium, arsine and phosphine, respectively, and said semiconductor substrate be fed with the trimethylgarium in high flow speed at low-growth temperature and pressure to effect monatomic layer growth through decomposition reaction on said substrate's surface.

## **LEGAL STATUS**

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2000 Japan Patent Office